

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	916	surface near mounting near circuit	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/20 15:49
L2	42314	substrate and terminal and resistor	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/20 15:51
L3	33399	"338"/\$.ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/20 15:51
L4	2663	point near symmetrical	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/20 15:52
L5	80	1 and 2	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/20 15:51
L6	0	4 and 5	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/20 15:52
L7	1	3 and 5	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/20 15:52
L8	83	2 and 4	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/20 15:52
L9	1	3 and 8	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/20 15:53
L10	2	1 and 4	US-PGPUB; USPAT; USOCR	OR	ON	2006/02/20 15:53

Day : Monday
Date: 2/20/2006

Time: 15:58:08

 PALM INTRANET

Inventor Information for 10/506947

Inventor Name	City	State/Country
KOBAYASHI, EIJI	NAGANO	JAPAN

Appln Info	Contents	Petition Info	Atty/Agent Info	Continuity Data	Foreign Data
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Search Another: Application# or Patent#

PCT / / or PG PUBS #

Attorney Docket #

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